

**Amendment and Response**

Applicant: Robert Walker

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Title: CHIP TO CHIP INTERFACE

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**IN THE SPECIFICATION**

Please replace the paragraph starting on page 4 at line 3 with the following rewritten paragraph:

In one embodiment, the clock signal is a 1 GHz clock signal and the DDR data signal is a 2 ~~GHz~~ Gbit DDR data signal. In another embodiment, the clock signal is a 1.6 GHz clock signal and the DDR data signal is a 3.2 ~~GHz~~ Gbit DDR data signal.